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"If you are passionate to work in Digital VLSI Verification domain, often wonder what it takes to land a job as Verification Engineer, and want to know what skills are required to crack a VLSI Verification interview: this book is a must-have for you!Having a good understanding of VLSI Design/Verification concepts and insights into VLSI ...

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Keeping this problem statement and these questions in their minds, authors (Ramdas M and Robin Garg) have recently launched a book: "Cracking Digital VLSI Verification Interview: Interview Success", e-book version of which is now available on Amazon. This book enables the readers to practice and grasp important concepts that are applicable to Digital VLSI Verification domain (and Interviews) through a Question and Answer approach with detailed explanations of concepts.

Cracking Digital VLSI Verification Interviews: Interview ...

Ramdas. Experienced Verification Engineer. Experienced Verification Engineer passionate in continuous learning and knowledge sharing (www.verificationexcellence.in) Co-Author of the book " Cracking Digital VLSI Verification Interview: Interview Success " - A Golden reference guide for VLSI engineers at all experience level Teaching Online courses on SystemVerilog, Assertions, Coverage, UVM Quora Top Writer - https://www.guora.com/profile/Ramdas-Mozhikunnath.

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Cracking Digital VLSI Verification Interview: Interview ...

The primary focus of the book - " Cracking Digital VLSI Verification Interview: Interview Success " - is for a Verification Interview. However some of the chapters do cover topics which are relevant event for a design interview (specifically HDL, logic design, computer architecture etc)

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